

Memorandum of Understanding
Between
VLSI Society of India (VSI)
And
Oriental University, Indore (OUI)

March 18th, 2024



R. Lika

R. Jadar

This Memorandum of Understanding ("MoU") entered, at Oriental University Indore, India on this **March 18th, 2024**, By and between

"VLSI Society of India (VSI)", a society promoting VLSI and Semiconductor education and research in India, Represented by Dr. Satya Gupta, President VSI

AND

"Oriental University, Indore", its address at Oriental University, Indore, Gate No.1, Sanwer Road, Opposite Revati Range, Jakhya, Indore, Madhya Pradesh 453555- hereinafter referred to as "OUI" Represented by Dr. Pradyumna Yadav, Registrar.

Scope and Purpose:

This MoU is being entered between VSI and OUI to define a partnership that will work together towards developing Talent, Research, Start-ups, and employment opportunities in the areas of VLSI Design and Semiconductors. Both the participating organizations will work together in collaboration to achieve the following objectives:

1. VSI has more than 5000+ semiconductor professionals and volunteers around the world representing full semiconductor supply chain. Working in collaboration with OUI, the partnership will help in holistic growth of semiconductor ecosystem for Madhya Pradesh region to build the required talent pipeline for the current and future growing needs that the India Semiconductor Industry is witnessing.
2. VSI and OUI will work together in developing and enhancing the VLSI Design and Semiconductors curriculum at undergraduate, post graduate and doctorate level and make these programs relevant for current and future needs of the industry. VSI will also provide help in implementing these courses.
3. VSI will provide help in setting up the required lab infrastructure for conducting the courses and projects in an effective manner.
4. VSI shall support through their industry volunteer network in providing expert lectures, projects, internship opportunities and mentoring the OUI students who are taking these courses at undergraduate, post-graduate, or doctorate level.
5. VSI and OUI will collaborate in promoting and helping research activities in VLSI Design and Semiconductors domains for current and future technology needs and trends.
6. VSI and OUI will work together for faculty development, faculty training and enhancing knowledge of the faculty members in VLSI Design and Semiconductor Areas.

Satya Gupta -



7. VSI and OUI will jointly hold workshops and training programs for students and faculty members from OUI and other institutions in the Madhya Pradesh Region.
8. VSI and OUI will work together to promote awareness regarding Electronics and Semiconductors and the career opportunities among the high school students and parents so that the students give preference to these areas while choosing their engineering branches.
9. OUI will provide help with VSI Madhya Pradesh Regional chapter in promoting VLSI Design and Semiconductors in the region, help organizing and participating in VSI organized conferences, workshops, and events.

This MoU shall be effective from the date of its execution March 18th, 2024, and shall remain in force for a period of 5 years. The parties may extend the term in writing. The MoU may be terminated by either party by giving a written notice of 30 days to the other party, without assigning any reason thereof.

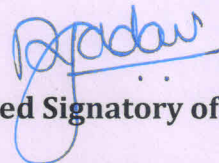
Parties have agreed to execute this MOU to deliver specific objectives on best efforts basis only with no financial transactions involved. Financial obligations arise/ involved, if any, will be addressed through separate agreement/ Contract.

In witness whereof, the parties hereto have offered their signature from the date mentioned above.



Authorized Signatory for VSI

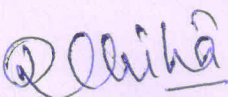
Dr. Satya Gupta
President, VLSI Society of India

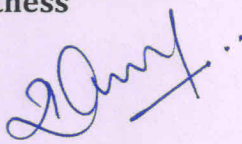


Authorized Signatory of OU, Indore

Dr. Pradyumna Yadav
Registrar, Oriental University, Indore

REGISTRAR
ORIENTAL UNIVERSITY
INDORE

Witness 
(CHITRA HARIHARAN)
Secretary - VSI

Witness 
Rahul Jain
AR, OUI, Indore